

# A Pseudorandom Binary Generator Based on Chaotic Linear Feedback Shift Register

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**Abstract:** *This paper presents a simple method for the design of Chaotic Linear Feedback Shift Register (CLFSR) system. The proposed method is based on a combination of two known systems. The first is called Linear Feedback Shift Register (LFSR) system, and the other is called Chaotic Map system. The main principle of the proposed system is that, the output of the LFSR is modified by exclusive-or (XOR) it with the stream bit that is generated by using the chaotic map system to eliminate the linearity and the repeating in the output of the LFSR system. The proposed system is built under Matlab environment and the quality of sequence generation tested by using standard tests which shows that the proposed system is a good random number generator that overcome the linearity and repeating disadvantages.*

**Keywords:** *LFSR, Chaotic Map, PRNG, Chaotic Binary Sequence Generator.*

## I. INTRODUCTION

The information security is one of the famous concepts of the most modern communication systems and computer networks. Therefore, the studies of encryption systems are one of the most important fields of scientific research in the last two decades. One of the major important aims of these scientific researches is to design high quality PseudoRandom Number Generation (PRNG), which is a basic subject of any encryption system [1].

The linear feedback shift registers (LFSRs) gives an economic, fast, and efficient method for generating a wide variety of pseudorandom number sequences [2]. In spite of the advantages in hardware complexity, this architecture has significant drawback that each bit in a LFSR's sequence is linearly related to its initial state which causes a big possibility of hacking [3].

There are three proposed methods to eliminate this linearity in the literature. In the first one, the linearity is eliminated by using several LFSRs, and the key stream is generated by a suitable nonlinear Boolean function. The second method is based on using single LFSR only, and the key stream is generated from nonlinear Boolean

function of the different stages of LFSR. Finally, the last method based on the use of irregularly clocked of the LFSR to eliminate the linearity. The main idea behind a clock-controlled generator is to introduce nonlinearity into LFSR-based key stream generators by means of having the output of one LFSR control on the clocking of a second LFSR [4].

On the other hand, the Chaotic Binary Sequence Generator (CBSG) based on the chaotic map is another method to generate pseudorandom number signals. The random-like, high non-linearity and unpredictable dynamics of chaotic systems, their inherent determinism and simplicity of realization suggests their potential for exploitation as PRNGs [1]. However, the main disadvantage of the CBSG is that the initial condition can be estimated based on a binary sequence [5].

In this paper, a new method proposed to eliminate the linearity in LFSR as well as to hide the statistical characteristic pattern of the CBSG that is used to estimate the initial condition of the chaotic map (see [6] for more details). The new-proposed method is based on mixing the two previous methods. The output of the LFSR will be exclusive-or with the output of the CBSG. The result sequence is used as a key stream. The proposed method herein has all the advantages of





of each 4 bit segment (*i.e.*:  $i \in [0 - 15]$ )

- The occurrences of each of the 16 possible 4 bit segment values are counted and stored in  $g(i)$ , where  $0 < i < 15$ , as the number of each 4 bit value.
- Finally, the test result is evaluated by the following equation:

$$I = \left( \left( \frac{16}{5000} \right) \left( \sum_{i=0}^{15} g(i)^2 \right) \right) - 5000 \quad (4)$$

If  $1.03 < I < 57.4$  then the sequence is random.

- 3) *The runs test*: The run can be defined as the repeated of the same bit in contiguous bits. The tested system can pass this test if the number of runs of length 1, 2, 3, 4, 5, and longer than 5 lies in specified limits described in Table II.

Table II

The runs test interval required under FIPS 140-2[17].

Length of run	Requiren Interval
1	2315-2685
2	1114-1386
3	527-723
4	240-384
5	103-209
Longer than 5	103-209

- 4) *The long run test*: The tested sequence is random if there are no run of length equal to or greater than 34 bits.

The results of all tests are reported in Table III. It can be noticed that the chaotic map systems and the CLFSR systems passes these tests and the sequences produced from them are random sequences. However, as shown from the previous tests any one of the chaotic map can be used to eliminate the linearity and the repeated in the output of LFSR to get a good pseudorandom generator. On the other hand the LFSR system cannot pass these tests. The shadow cells in the test table refer to the reject test results. Therefore, the sequence generated by it cannot be classified as a random sequence.

The simple visual test is another test done by the graphically representation of the data. In this

test, the  $(320 \times 320 \times 8 = 819,200)$  bits from the test sequences are divided to the  $(320 \times 320 = 102,400)$  consecutive 8 bits. Each 8 bits is read as a single unsigned integer and the resulted value (0-255) is plotted as a pixel brightness (0 being black) on a grayscale image from  $320 \times 320$  images. The patterns on the image will be shown if there is a presence for the periodic or non-randomness in the sequence.

The simple visual test is excellent in detecting large-scale periodic or patterns in the data generated by a system. Table IV shows the image built from the sequences generated from studied systems. The results clearly show that the CLFSR is a random sequence and the pattern shown in LFSR system is referred to the periodic natural in the sequence, which is not appeared in the proposed system.

## VI. CONCLUSION

In this paper, a pseudorandom number generator is proposed based on CLFSR system. The proposed system was built by using Matlab program and the quality of its output sequence was tested by comparing its performance with the performances of the LFSR and the chaotic map systems.

Two types of tests are used to verify the performance of the system, the first one, named FIPS 140-2, which consist from four tests used to verify the randomness of bit sequences. The other one is the visual test, which is used to detect the large-scale periodic or patterns in the bit sequences generated by the system. The results clearly show that the performance of the proposed system is better than that of LFSR in eliminating the linearity and the repeating in the output stream bit. However, the simulation results show that any one of the chaotic map can be used here.

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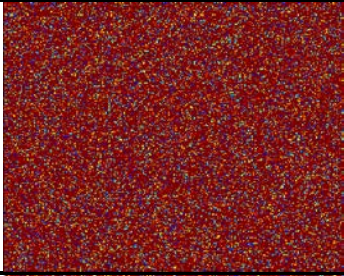
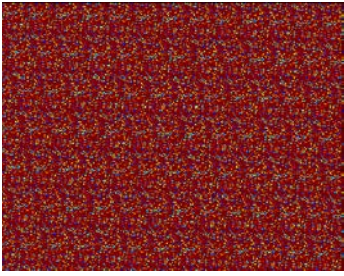
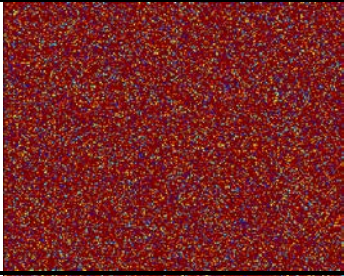
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Table III  
Randomness Test of the Different Systems Studies

System Under Test	Monobit Test	Poker Test	Runs Test						Long Runs Test
			L=1	L=2	L=3	L=4	L=5	L=6	
Acceptance <sup>*</sup> Range	9,725-10,275	2.16-46.17	2,315-2,685	1,114-1,386	527-723	240-384	103-209	103-209	0
Logistic map <sup>(1)</sup> system	10074	10.992	2490	1223	629	328	171	158	0
Quadratic map <sup>(2)</sup> system	10168	12.9632	2453	1268	585	314	171	150	0
LFSR system	9999	-0.9312	2499	1245	629	313	156	160	0
CLFSR based on (1)	9995	15.824	2630	1205	611	310	163	153	0
CLFSR based on (2)	10029	12.0672	2420	1276	654	294	179	162	0

\* The test results must be located in the range of acceptance to ensure that the sequence under test is random.

Table IV  
The Simple Visual Analysis of the Different Systems Studies

	Chaotic Map system	LFSR system	CLFSR system
Logistic map			
Quadratic map	